

EAST Search History

EAST Search History (Prior Art)

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	224	du.xa.	USPAT	OR	OFF	2006/11/29 15:55
S2	245	(thuan near2 (du su)).xa.xp.	USPAT	OR	OFF	2005/09/26 13:09
S4	218	(thuan near2 (du su)).xa.	USPAT	OR	OFF	2005/09/21 13:59
S5	171	(thuan near2 (du su)).xp.	USPAT	OR	OFF	2008/04/13 11:17
S6	5	"5487097"	USPAT	OR	OFF	2009/07/01 16:25
S7	257	(thuan near2 (du su)).xp.	USPAT	OR	OFF	2009/07/17 08:09
S8	24320	(cpu process\$3) near3 reset\$4	USPAT	OR	OFF	2009/09/30 14:23
S9	38984	(cpu process\$3) near3 reset\$4	US-PGPUB; USPAT	OR	OFF	2009/09/30 14:24
S10	206	(cpu process\$3) near3 reset\$4 with selectively	US-PGPUB; USPAT	OR	OFF	2009/09/30 14:37
S11	59482	address near3 (list \$1 table)	US-PGPUB; USPAT	OR	OFF	2009/09/30 14:38
S12	0	S10 same S11	US-PGPUB; USPAT	OR	OFF	2009/09/30 14:38
S13	27	S10 and S11	US-PGPUB; USPAT	OR	OFF	2009/09/30 14:38
S14	511051	data near3 (memory \$2ram)	US-PGPUB; USPAT	OR	OFF	2009/09/30 14:38
S15	10235	data near3 (memory \$2ram) with retain\$3	US-PGPUB; USPAT	OR	OFF	2009/09/30 14:39

S16	22	S13 and S15	US-PGPUB; USPAT	OR	OFF	2009/09/30 14:39
S17	47	reset\$4 near2 (code\$1 instruction \$1) same (associat \$3 correspond\$3) near2 (process\$3 cpu)	USPAT	OR	OFF	2009/10/02 07:57
S18	27	reset\$4 near2 (code\$1 instruction \$1) same (associat \$3 correspond\$3) near2 (processor cpu)	USPAT	OR	OFF	2009/10/02 07:57
S19	43	reset\$4 near2 (code\$1 instruction \$1) same (associat \$3 correspond\$3) near2 (processor cpu)	US-PGPUB; USPAT	OR	OFF	2009/10/02 07:57
S20	11	reset\$4 near2 (code\$1 instruction \$1) with (associat \$3 correspond\$3) near2 (processor cpu)	US-PGPUB; USPAT	OR	OFF	2009/10/02 07:57
S21	0	reset\$4 near2 (code\$1 instruction \$1) with (associat \$3 correspond\$3) near2 (processor cpu) same address near3 (table list)	US-PGPUB; USPAT	OR	OFF	2009/10/02 16:17
S22	27	data near3 (memory \$2ram) with (retain\$3 maintain\$4 surviv \$4) with (while during) with (reboot\$3 reset \$4)	US-PGPUB; USPAT	OR	OFF	2009/10/19 15:16

S23	1	data near3 (memory \$2ram) with (retain\$3 maintain\$4 surviv \$4) with (while during) with (reboot\$3 reset \$4) and 713/1,2,100.ccls.	US- PGPUB; USPAT	OR	OFF	2009/10/19 16:20
S24	5	reset\$4 near2 (code\$1 instruction \$1) same (associat \$3 correspond\$3) near2 (processor cpu) and 713/1,2,100.ccls.	US- PGPUB; USPAT	OR	OFF	2009/10/19 16:22

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